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Applicants: Ko MIYAZAKI, et al.

Filed: September 28, 2001

For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**CLAIM FOR PRIORITY**

Assistant Commissioner for Patents  
Washington, D.C. 20231

September 28, 2001

Sir:

Under the provisions of 35 USC §119 AND 37 CFR § 1.55, Applicants hereby claim the right of priority based on Patent Application No. 2000-308320 filed in Japan on October 6, 2000.

Respectfully submitted,

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